

Notice of References Cited	Application/Control No. 10/807,499		Applicant(s)/Patent Under Reexamination BUTCHER ET AL.	
	Examiner Aimee J. Li		Art Unit 2183	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-6,484,314	11-2002	Ishizaki et al.	717/151
*	B	US-5,889,982	03-1999	Rodgers et al.	712/229
*	C	US-6,014,723	01-2000	Tremblay et al.	711/1
*	D	US-6,789,098	09-2004	Dijkstra, Wilco	708/495
*	E	US-5,727,227	03-1998	Schmidt et al.	712/36
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)				
	U	Free On-Line Dictionary of Computing. FOLDOC. ©1995-1998. www.foldoc.org search terms: central processing unit; arithmetic and logic unit; control unit; and machine cycle				
	V	Wikipedia, the free encyclopedia. ©2003. www.wikipedia.com search term: protected mode				
	W	Hyde, Randall. "The Art of Assembly Language Programming". ©1996. http://www.arl.wustl.edu/~lockwood/class/cs306/books/artofasm/toc.html Chapters: Table of Contents; Chapter 6 Part 2; and Chapter 6 Part 5.				
	X					

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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